UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO. : 6.963,103 B2 DATED

: November 8, 2005

INVENTOR(S) : Forbes

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

Item [56], References Cited, U.S. PATENT DOCUMENTS, below "5,981,350 A 11/1999 Geusic et al. 438/386" insert -- 5,986,932 A* 11/1999 Ratnakumar et al 365/185.07 --. OTHER PUBLICATIONS, "Grimbolt, J.," reference, delete "Grimbolt," and insert

-- Grimblot, --.

Column 24,

Lines 45-46, delete "separate" and insert -- separated --. Line 49, delete "intergrate" and insert -- intergate --.

Column 25,

Lines 3-4 and 24-25, delete "separate" and insert -- separated --.

Line 47, delete "transistor" and insert -- transistors --.

Line 48, after "and" insert -- a --.

Line 56, delete "intergrate" and insert -- intergate --.

Column 26,

Lines 13 and 43, delete "transistor" and insert -- transistors --.

Line 19, delete "rate" and insert -- gate --.

Column 27,

Line 5, after "substrate" insert --; --.

Lines 6-7, delete "separate" and insert -- separated --.

Line 22, delete "elected" and insert -- selected --.

Line 60, delete "transistor" and insert -- transistors --.

Column 28,

Lines 6-7, delete "transistor" and insert -- transistors; --.

Lines 23 and 36-37, delete "transistor" and insert -- transistors --.

Lines 26-27 and 53-54, delete "separate" and insert -- separated --.

Line 48, after "SRAM" insert -- cell --.

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Page 2 of 2

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 30,

Lines 11-12, delete "separate" and insert -- separated --.

Signed and Sealed this

Twenty-fifth Day of April, 2006

JON W. DUDAS Director of the United States Patent and Trademark Office